

Main Claim

1. Method for measuring and compensating skews of data transmission lines connecting at least one data
5 transmission device with a data reception device via a parallel data bus comprising for each data transmission line the following steps:

measuring the relative time delay of the data
10 transmission line by transmitting a determined sequence of measurement vectors (MV) each consisting of an alternating bit pattern via said data transmission line, wherein the bit alternation frequency is halved with every transmitted measurement vector (MV);
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comparing the received measurement vectors (MV') transmitted via said data transmission line with corresponding reference vectors (RV) stored in said data reception device;
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shifting the received measurement vectors by inserting data unit intervals (UI) until a received measurement vector (MV') matches a corresponding reference vector (RV);
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calculating a relative skew of the data transmission line depending of the number of inserted data unit intervals (UI) with respect to a slowest data transmission line;
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and compensating the calculated relative skew of the data transmission line by means of delay elements switched in response to the calculated relative skew.

2. The method for measuring and compensating skews of a data transmission lines connecting at least one data transmission device and a data reception device according to claim 1 comprising for each data transmission line the
5 following steps:

- a) initializing an iteration loop counter (i) and an insertion counting variable to zero
($i:=0$;insert variable:= \emptyset);
10
- b) activating the data transmission device to transmit a measurement vector (MV_i) consisting of an alternating bit pattern with a predetermined bit alternation frequency via said data transmission line to said data
15 reception device,
wherein each bit is transmitted during a predetermined data unit interval (UI),
wherein the measurement vector (MV_i) corresponds to a reference vector (RV_i) stored in said data reception
20 device;
- c) comparing the measurement vector (MV_i) received by the data reception device via said data transmission line with the stored reference vector (RV_i);
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- d) wherein until the received measurement vector (MV_i') and the stored reference vector (RV_i) do match the following sub-steps are performed in an iteration loop:
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 - d1) activating the data transmission device to send a next measurement vector (MV_{i+1}) having half the bit alternation frequency of the preceding measurement vector (MV_i),

- d2) substituting the reference vector (RV_i) to be compared by a next reference vector (RV_{i+1}) which corresponds to the next measurement vector (MV_{i+1}),
- 5 d3) inserting a number (N) of data unit intervals (UI) by shifting the received measurement vector (MV_{i+1}) by means of a shift register, wherein the number (N) of inserted data unit intervals (UI) is 2 given by the insertion counting variable ($insert := insert + 2^i$)
- 10 d4) incrementing the iteration loop counter ($i := i + 1$);
- 15 e) and calculating the relative skew of the data transmission line depending on the number of inserted data unit intervals (UI) counted by said insertion variable (insert).
- 20 3. The method for measuring and compensating skews of a data transmission lines according to claim 2, wherein for calculating the relative skew of the data transmission line in step (e) the following sub-steps are performed:
- 25 e1) incrementing the iteration loop counter (i) ($i = i + 1$);
- e2) activating the data transmission device to transmit a next measurement vector having half the bit alternation frequency of the last measurement vector transmitted in the iteration loop of step (d) via said data transmission line to said data reception device;
- 30 e3) substituting the last reference vector (RV) used in the iteration loop of step (d) by a reference vector

which corresponds to the measurement vector transmitted in step (e2);

e4) comparing the measurement vector (MV') received by
5 the data reception device with the reference vector substituted in step e3);

e5) wherein the skew of the data transmission line is calculated as:

10 skew = 2^i -insert

in case that the vectors compared in step (e4) do not match and as:

15 skew= -insert

in case that the vectors compared in step (e4) do match,
20 wherein insert is the insertion variable accumulated in step (d4).

4. The method for measuring and compensating skews of data transmission lines according to claim 1 wherein the
25 method is performed when the data reception device is powered up.

5. The method according to claim 1 wherein after compensation the skew of all data transmission lines is
30 accomplished data are transmitted from the data transmission devices to the data reception device in a normal data transfer mode.

6. The method according to claim 1 wherein the data transmission devices are DRAMs.

7. The method according to claim 1 wherein the data
5 reception device is a HUB of a memory module.

8. The method according to claim 1 wherein the data reception device comprises for each data transmission line a clock and data recovery unit to lock to the first
10 measurement vector transmitted via said data transmission line.

9. The method according to claim 1 wherein for compensating the skew of a data transmission line the
15 following sub-steps are performed:

determining the maximum skew (skew max) of the calculated
skews of all data transmission lines;

20 calculating a relative skew of each data transmission line with respect to the maximum skew of the slowest data transmission line,

and delaying each data transmission line with its
25 calculated relative skew.

10. The method according to claim 1 wherein the data transmission devices are activated by means of a request signal sent via a separate command line from the data
30 reception device to the data transmission devices.

11. The method according to claim 10 wherein the data transmission devices are activated simultaneously.

12. The method according to claim 1 wherein the data transmission lines form part of a bi-directional data bus.

- 5 13. The method according to claim 1 wherein the comparing of the received measurement vectors (MV') and the reference vectors (RV) is performed by means of an EXOR logic.

Reference list

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|----|----|-------------------------------|
| | 1 | DRAM |
| | 2 | memory buffer |
| 5 | 3 | data transmission |
| | 4 | data transmission device |
| | 5 | data reception device |
| | 6 | data source |
| | 7 | measurement pattern generator |
| 10 | 8 | control unit |
| | 9 | control line |
| | 10 | multiplexer |